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2005 SEP 11

TO: USPTO (To whom it may concern)

Re: Application Number 10/081,963

Number of pages including this one: 2

## Message

Page 2 of this FAX shows that

- i) the Issue Fee Payment for this application was recorded on 04-26-2005
- ii) a Notice of Allowance and Fees Due (PTOL-85) was issued on 06-17-2005

Examination of the File Wrapper Image of the 06-17-2005 Notice of Allowance and Fees Due (PTOL-85) shows that it was actually a Supplemental Notice of Allowability (PTOL-85) that was issued on 06-17-2005, and that no Fees Due was issued on that date. Therefore, it appears that all requirements for the Patent to issue have been satisfied. If so, please let me know when the Patent will issue. If not, please let me know what else is needed.

Regards,



Jon Geist  
Applicant Pro Se



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10/081,963

METHOD FOR TMAH ETCHING OF CMOS INTEGRATED CIRCUIT



This application is officially maintained in electronic form. To View: Click the desired Document. To Print: Check the desired document(s) and click StartDownload.

Mail Room Date	Document Description	Page Count
06-17-2005	Notice of Allowance and Fees Due (PTOL-85)	
06-17-2005	Examiner Interview Summary Record (PTOL - 413)	
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01-24-2005	Notice of Allowance and Fees Due (PTOL-85)	
01-24-2005	Notice of Allowance and Fees Due (PTOL-85)	
01-24-2005	Claims	
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